

FORM PTO-1390

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

ATTORNEY'S DOCKET NUMBER

**TRANSMITTAL LETTER TO THE UNITED STATES
DESIGNATED/ELECTED OFFICE (DO/EO/US)
CONCERNING A FILING UNDER 35 U.S.C. 371**

43890-509

U.S. APPLIC. NO. (if known, see 37 CFR 1.5)

09/830493

INTERNATIONAL APPLICATION NO.

INTERNATIONAL FILING DATE

PRIORITY DATE CLAIMED

PCT/JP00/06460

September 21, 2000

September 22, 1999

TITLE OF INVENTION

ELECTRONIC DEVICE OF CERAMIC

APPLICANT(S) FOR DO/EO/US

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Applicant herewith submits to the United States Designated/Elected Office (DO/EO/US) the following items and other information:

1. ☒ This is a **FIRST** submission of items concerning a filing under 35 U.S.C. 371
2. ☐ This is a **SECOND** or **SUBSEQUENT** submission of items concerning a filing under 35 U.S.C. 371.
3. ☒ This express request to begin national examination procedures (35 U.S.C. 371(f)) at any time rather than delay examination until the expiration of the applicable time limit set in 35 U.S.C. 371(b) and PCT Articles 22 and 39(1)
4. ☐ A proper Demand for International Preliminary Examination was made by the 19th month from the earliest claimed priority date.
5. ☐ A copy of the International Application as filed (35 U.S.C. 371(c)(2))
 - a. ☐ is transmitted herewith (required only if not transmitted by the International Bureau).
 - b. ☐ has been transmitted by the International Bureau.
 - c. ☐ is not required, as the application was filed in the United States Receiving Office (RO/US)
6. ☒ A translation of the International Application into English (35 U.S.C. 371(c)(2))
7. ☐ Amendments to the claims of the International Application under PCT Article 19 (35 U.S.C. 371(c)(3))
 - a. ☐ are transmitted herewith (required only if not transmitted by the International Bureau).
 - b. ☐ have been transmitted by the International Bureau.
 - c. ☐ have not been made; however, the time limit for making such amendment has NOT expired
 - d. ☐ have not been made and will not be made.
8. ☐ A translation of the amendments to the claims under PCT Article 19 (35 U.S.C. 371(c)(3)).
9. ☐ An oath or declaration of the inventor(s) (35 U.S.C. 371(c)(4)).
10. ☐ A translation of the annexes to the International Preliminary Examination Report under PCT Article 36 (35 U.S.C. 371(c)(4)).

Items 11. to 16. below concern other document(s) or information included:

11. ☒ An Information Disclosure Statement under 37 CFR 1.97 and 1.98.
12. ☐ An assignment document for recording. A separate cover sheet in compliance with 37 CFR 3.28 and 3.31 is included.
13. ☐ A FIRST preliminary amendment
A SECOND or SUBSEQUENT preliminary amendment
14. ☐ A substitute specification
15. ☐ A change of power of attorney and/or address letter
16. ☒ Other items or information.

International Search Report prepared by JPO
Forms PCT/IB/301 and PCT/IB/304
Front page of published International application



20277

PATENT TRADEMARK OFFICE

U.S. APPLIC. NO. (if known, see 37 CFR 1.50) <div style="font-size: 24pt; font-weight: bold; margin-top: 5px;">09/830493</div>	INTERNATIONAL APPLICATION NO. PCT/JP00/06460	ATTORNEY'S DOCKET NUMBER 43890-509
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	CALCULATIONS	PTO USE ONLY
17. <input checked="" type="checkbox"/> The following fees are submitted.		
Basic National Fee (37 CFR 1.492(a)(1)-(5)): Search Report has been prepared by the EPO or JPO \$860.00		
International preliminary examination fee paid to USPTO (37 CFR 1.482) \$690.00 No international preliminary examination fee paid to USPTO (37 CFR 1.482) but international search fee paid to USPTO (37 CFR 1.445(a)(2)) \$710.00		
Neither international preliminary examination fee (37 CFR 1.482) nor international search fee (37 CFR 1.445(a)(2)) paid to USPTO \$1,000.00		
International preliminary examination fee paid to USPTO (37 CFR 1.482) and all claims satisfied provisions of PCT Article 33(2)-(4) \$100.00		
ENTER APPROPRIATE BASIC FEE AMOUNT =		\$ 860.00
Surcharge of \$130.00 for furnishing the oath or declaration later than <input checked="" type="checkbox"/> 20 <input type="checkbox"/> 30 months from the earliest claimed priority date (37 CFR 1.492(e)).		\$ 130.00
Claims	Number Filed	Number Extra
Total Claims	24 -20 =	4
Independent Claims	4 -3 =	1
Multiple dependent claim(s) (if applicable)		+ \$270.00
TOTAL OF ABOVE CALCULATIONS =		\$ 1,142.00
Reduction by 1/2 for filing by small entity, if applicable. Verified Small Entity Statement must also be filed. (Note 37 CFR 1.9, 1.27, 1.28).		\$
SUBTOTAL =		\$ 1,142.00
Processing fee of \$130.00 for furnishing the English translation later than the <input type="checkbox"/> 20 <input type="checkbox"/> 30 months from the earliest claimed priority date (37 CFR 1.492(f)).		\$
TOTAL NATIONAL FEE =		\$ 1,142.00
Fee for recording the enclosed assignment (37 CFR 1.21(h)). The assignment must be accompanied by an appropriate cover sheet (37 CFR 3.28, 3.31). \$40.00 per property		\$
TOTAL FEES ENCLOSED =		\$ 1,142.00
		Amount to be: refunded \$
		charged \$
a. <input type="checkbox"/> A check in the amount of \$ _____ to cover the above fees is enclosed.		
b. <input checked="" type="checkbox"/> Please charge my Deposit Account No. <u>500417</u> in the amount of \$ <u>1,142.00</u> to cover the above fees. A duplicate copy of this sheet is enclosed.		
c. <input checked="" type="checkbox"/> The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account No. <u>500417</u> . A duplicate copy of this sheet is enclosed.		
NOTE: Where an appropriate time limit under 37 CFR 1.494 or 1.495 has not been met, a petition to revive (37 CFR 1.137(a) or (b)) must be filed and granted to restore the application to pending status.		
SEND ALL CORRESPONDENCE TO:		
McDERMOTT, WILL & EMERY 600 13 th Street, N.W. Washington, DC 20005-3096 (202) 756-8000 Facsimile (202) 756-8087		
SIGNATURE Michael E. Fogarty		
NAME 36,139		
REGISTRATION NUMBER April 27, 2001		
DATE		

DESCRIPTION

Electronic Device of Ceramic

5 Technical Field

The present invention relates to a method of manufacturing ceramic electronic components such as multilayer ceramic capacitors and the like.

Background Art

10 Fig. 7 is a partially cut-away perspective view of a typical multilayer ceramic capacitor comprising a ceramic dielectric layer 1, a conductive layer 2 and a pair of external electrodes 3, in which an end of each respective conductive layer 2 is alternately connected with one of the pair of external electrodes 3 at the two side of the ceramic dielectric layer 1.

15 Next, a description is given to a method of manufacturing multilayer ceramic capacitors in a prior art.

First, a ceramic sheet eventually constituting the ceramic dielectric layer 1 is prepared by mixing an organic material to a powder mainly composed of barium titanate, and a metallic paste is applied thereon in a required pattern by a printing method to form the conductive layer 2. Then, a plurality of the ceramic sheets, each applied with the conductive layer 2 thereon, are superimposed one over another in such a way that any two adjoining conductive layers 2 are located opposite to each other with the ceramic sheet sandwiched therebetween, thus obtaining a laminate. The laminate is fired thereafter and a pair of the external electrodes 3 are formed on both side where the conductive layer 2 is exposed.

20 However, when the porosity of a ceramic sheet is large, the foregoing prior art method allows part of the metallic constituent in the metallic paste to penetrate into the ceramic sheet during the printing process to print directly the metallic paste on the ceramic sheet.

25 In recent years, a ceramic sheet for a multilayer ceramic capacitor is

made thinner and thinner in order to gain higher capacitance in said capacitor, thereby causing a problem of short-circuiting between adjoining two electrodes because of the metal constituent that has penetrated into the ceramic sheet.

The object of the present invention is to provide a method of manufacturing ceramic electronic component with less failures due to said short-circuiting.

Summary of the Invention

In order to solve the aforementioned problems, the method of manufacturing ceramic electronic components according to the present invention is characterized in that the porosity of a ceramic sheet is first reduced and then a conductive layer is formed on the surface thereof, thereby allowing a metallic constituent to be prevented from penetrating into the ceramic sheet with resulting prevention of short-circuiting failure that may otherwise occur between the conductive layers.

A method of manufacturing ceramic electronic components in a first exemplary embodiment of the present invention comprises:

a first step of applying a pressing force to a ceramic sheet containing a ceramic powder and an organic material to have the porosity thereof reduced;

a second step of forming a conductive layer on the ceramic sheet by the use of a metallic paste;

a third step of producing a laminate by stacking a plurality of ceramic sheets, each having the conductive layer formed thereon, in such a way as having the ceramic sheet sandwiched between the adjoining conductive layers located opposite to each other; and

a fourth step of firing the laminate, thus allowing a ceramic electronic component free of short-circuiting failures to be realized.

A method of manufacturing said components in a second exemplary embodiment of the present invention is the method of said components according to the first exemplary embodiment, in which the porosity of the ceramic sheet before a reduction in porosity after the first step is 50 % or more, thereby allowing a ceramic electronic component free of short-circuiting failures to be realized.

A method of manufacturing said components in a third exemplary embodiment of the present invention is the method of said components according to the first exemplary embodiment, in which the ceramic sheet contains at least a ceramic constituent and polyethylene at the first step, thereby achieving a great effect in reducing short-circuiting failures, because of a high level of porosity in the ceramic sheet containing polyethylene, compared with other organic materials.

A method of manufacturing ceramic electronic components in a fourth exemplary embodiment of the present invention is the method of said components according to the first exemplary embodiment, in which the porosity of a ceramic sheet at the first step is less than 50 %, thereby allowing a metallic constituent to be prevented from penetrating into the ceramic sheet.

A method of manufacturing ceramic electronic components in a fifth exemplary embodiment of the present invention is the method of said components according to the first exemplary embodiment, in which the pressing force applied to the ceramic sheet at the first step is made less than the pressing force as applied in forming the laminate at the third step, thereby allowing a sufficiently uniform pressing force to be applied to the entire laminate in both areas, with and without a conductive layer at the third step, resulting in a realization of an electronic component that has little structural defect due to failures in adhesion between the ceramic sheets.

A method of manufacturing ceramic electronic components in a sixth exemplary embodiment of the present invention is the method of said components according to the first exemplary embodiment, in which the ceramic sheet is heated while a pressing force being applied thereto at the first step, thereby allowing the fluidity of an organic material to be enhanced by the application of heat with a resulting quick reduction in porosity of the ceramic sheet.

A method of manufacturing ceramic electronic components in a seventh exemplary embodiment of the present invention is the method of said components according to the sixth exemplary embodiment, in which the ceramic sheet is heated at the temperature between the glass transition point and the melting point of at least one organic material contained in the ceramic sheet, thereby

allowing the fluidity of organic materials to be enhanced by the application of heat with a resulting quick reduction in porosity of the ceramic sheet.

A method of manufacturing ceramic electronic components in an eighth exemplary embodiment of the present invention comprises:

a first step of applying a pressing force to reduce the porosity in a ceramic sheet comprising a ceramic powder and an organic material ;
a second step of forming a conductive layer by a metallic paste on a base film in advance and superimposing the conductive layer on the ceramic sheet;
a third step of producing a laminate by stacking a plurality of the ceramic sheets, each having the conductive layer superimposed thereon, in such a way as having the ceramic sheet sandwiched between the adjoining conductive layers located opposite to each other; and
a fourth step of firing the laminate, thereby allowing a ceramic electronic component free of short-circuiting failures to be realized.

A method of manufacturing ceramic electronic components in a ninth exemplary embodiment of the present invention is the method of said components according to the eighth exemplary embodiment, in which the porosity of the ceramic sheet before the first step is 50 % or more, thereby allowing a said component free of short-circuiting failures to be realized.

A method of manufacturing ceramic electronic components in a tenth exemplary embodiment of the present invention is the method of said components according to the eighth exemplary embodiment, in which the ceramic sheet is prepared so as to contain at least a ceramic powder and polyethylene at the first step, thereby achieving a great effect in reducing short-circuiting failures.
The rate of volumetric shrinkage of the ceramic sheet after the application of a pressing force thereto is made uniform in those composition because of the high level in porosity of the ceramic sheet before applying the pressing force thereto and allowing a further reduction in porosity of the ceramic ceramic sheet to be achieved.

A method of manufacturing ceramic electronic components in an eleventh exemplary embodiment of the present invention is the method of said components according to the eighth exemplary embodiment, in which the porosity of

the ceramic sheet after the first step is less than 50 %, thereby allowing the metallic constituent to be prevented from penetrating into the ceramic sheet.

A method of manufacturing ceramic electronic components in a twelfth exemplary embodiment of the present invention is the method of said components according to the eighth exemplary embodiment, in which the pressing force applied to the ceramic sheet at the first step is made less than the pressing force as applied in forming the laminate at the third step, thereby allowing a sufficiently uniform pressing force to be applied to the entire laminate in both region with and without the conductive layer at the third step, resulting in a realization of an electronic component that has little structural defect due to failures in adhesion between the ceramic sheets.

A method of manufacturing ceramic electronic components in a thirteenth exemplary embodiment of the present invention is the method of said components according to the eighth exemplary embodiment, in which the ceramic sheet is heated while a pressing force being applied thereto at the first step, thereby allowing the fluidity of the organic material to be enhanced by the heat application with a resulting quick reduction in porosity of the ceramic sheet.

A method of manufacturing ceramic electronic components in a fourteenth exemplary embodiment of the present invention is the method of said components according to the eighth exemplary embodiment, in which the ceramic sheet is heated at the temperature between the glass transition point and the melting point of at least one organic material contained in the ceramic sheet, thereby allowing the fluidity of organic materials to be enhanced by the heat application with a resulting quick reduction in porosity of the ceramic sheet.

A method of manufacturing ceramic electronic components in a fifteenth exemplary embodiment of the present invention is the method of said components according to the eighth exemplary embodiment, in which the amount of an organic constituent in the conductive layer before lamination process is from 5 wt % to 15 wt % against 100 wt % of the metallic constituent at the third step, thereby allowing the sufficient adhesion between the conductor and the ceramic sheet to be realized, resulting in obtaining a ceramic component that has little structural defect.

A method of manufacturing ceramic electronic components in a sixteenth exemplary embodiment of the present invention is the method of said components according to the eighth exemplary embodiment, with additional step of applying a pressing force to the conductive layer in the thickness direction thereof placed between the second step and the third step, thereby allowing the microscopic asperities on the surface of the conductor layer to be reduced and contributing further to the prevention of short-circuiting failures.

A method of manufacturing ceramic electronic components in a seventeenth exemplary embodiment of the present invention comprises:

a first step of preparing a laminate by stacking ceramic sheets, each composing an organic material and a ceramic powder and conductive layers one over another alternately; and

a second step of firing the laminate, in which the ceramic sheet is formed with the organic material arranged horizontally in a mesh-like structure and with the organic material and ceramic powder arranged vertically at random, thereby allowing a metallic constituent to be prevented from penetrating the ceramic sheet with a resulting contribution to realizing a ceramic electronic component which is less susceptible to short-circuiting failures.

A method of manufacturing ceramic electronic components in an eighteenth exemplary embodiment is the method according to the seventeenth exemplary embodiment, in which the porosity of the ceramic sheet is less than 50 %, thereby allowing a metallic constituent to be prevented from penetrating the ceramic sheet.

A method of manufacturing ceramic electronic components in a nineteenth exemplary embodiment is the method according to the seventeenth exemplary embodiment, in which, after the conductive layer formed on a base film is superimposed on the ceramic sheet at the first step, a step of peeling off the base film and a step of superimposing the ceramic sheet on the conductive layer are repeated one after another, thereby allowing the solvent constituent in the conductive layer to be reduced when compared with the case where the conductive layer is formed directly on the ceramic sheet with a resulting contribution to preventing the metallic constituent from penetrating into the ceramic sheet.

A method of manufacturing ceramic electronic components in a twentieth exemplary embodiment of the present invention is the method according to the nineteenth exemplary embodiment, with an additional step of applying a pressing force to the conductive layer on the base film in the thickness direction before the conductive layer is superimposed on the ceramic sheet, thereby allowing the microscopic asperities on the surface of the conductor layer to be reduced, with a resulting contribution to preventing the metallic constituent from penetrating into the ceramic sheet.

A method of manufacturing ceramic electronic components in a twenty first exemplary embodiment of the present invention is the method of said components according to the seventeenth exemplary embodiment, in which the conductive layer is deposited by the thin film process at the first step, in this case a metallic film is grown into a plate-like shape on the ceramic sheet, thereby allowing the metallic constituent to be prevented from penetrating into the sheet.

A methods of manufacturing ceramic electronic components in a twenty second exemplary embodiment to a twenty fourth exemplary embodiment of the present invention comprise a step of preparing a laminate by stacking ceramic sheets, each containing an organic material and a ceramic powder, and conductive layers one over another alternately, in which the ceramic sheet is formed with the organic material arranged horizontally in a mesh-like structure and arranged at random in the thickness direction, thereby having the particles of the ceramic powder absorbed in the meshes of the organic material. As a result, the ceramic particles are also arranged at random, each presenting a mesh-like structure. Thus, a ceramic electronic component characterized as having a structure formed by stacking the ceramic layers, each having mesh-like structures of ceramic particles arranged at random, and the conductive layers one over another alternately shows a reduced number of short-circuiting pathes.

Brief Description of the Drawings

Fig. 1 is a partially enlarged cross-sectional view of a ceramic sheet before a reduction process in porosity.

Fig. 2 is a partially enlarged cross-sectional view of a ceramic sheet af-

ter a reduction in porosity according to the present invention.

Fig. 3 is a diagram to show the manufacturing process for a ceramic sheet according to the present invention.

Fig. 4 is a cross-sectional view of a conductive layer prepared on a base film before a surface smoothing treatment is applied thereto according to the present invention.

Fig. 5 is a cross-sectional view of a conductive layer after a surface smoothing treatment is applied thereto according to the present invention.

Fig. 6 is a cross-sectional drawing for illustrating the process of smoothing treatment to obtain the conductive layer as shown in Fig. 5.

Fig. 7 is a partially cut-away perspective view of a typical multilayer ceramic capacitor.

Preferred Mode for Carrying Out the Invention

Next, a description is given to exemplary embodiments of the present invention with respect to a multilayer ceramic capacitor as a typical ceramic electronic component with reference to drawings.

(First Exemplary Embodiment)

First, a ceramic sheet 10a of 10 μm thick is prepared by the use of a ceramic powder composing barium titanate as major component and polyethylene of an average molecular weight of 400,000 or more. The porosity of this ceramic sheet 10a usually is around 65 %. Fig. 1 is a partially enlarged cross-sectional view of the ceramic sheet 10a, showing that ceramic particles 11 are sporadically located among the fibers of polyethylene 12 with a lot of voids 13 formed therein. In addition, the polyethylene 12 is formed in a mesh-like structure extending horizontally and at random in the thickness direction, where the ceramic particles 11 are absorbed in the meshes, thereby eventually having the ceramic particles 11 also arranged in a mesh-like structure extending horizontally and at random vertically.

Next, as shown in Fig. 3, the ceramic sheet 10a is passed through between rollers 14a and 14b, each provided with a smooth surface. At this time, the roller 14a and roller 14b are, respectively, rotating in a direction opposite to each

other, and the ceramic sheet 10a is applied with a pressing force in the direction of thickness thereof and also heated. By having the ceramic sheet 10a applied with heat in addition to a pressing force, the porosity thereof is allowed to be reduced easily. More specifically, by an application of heat to said sheet 10a, the fluidity of polyethylene 12 is increased at the process of a pressing force application, resulting in having the air inside of said sheet 10a discharged outside and also become the density of the ceramic particles 11 higher. It is preferred that the heat treatment is carried out at the temperature between a glass transition point of polyethylene and the melting point thereof. More specifically, the optimum heating temperature ranges from 60 °C to 150 °C

Instead of using the rollers 14a and 14b, a press plate is allowed to be used to perform one axis pressing in the thickness direction, to reduce the porosity of the ceramic sheet 10a. The rollers 14a and 14b are used for continuous processing. In the present exemplary embodiment, test ceramic sheets 10b are prepared in four kinds of porosity, i.e., 40 %, 30 %, 20 % and 10 %.

Fig. 2 is a partially enlarged cross-sectional view of the ceramic sheet 10b after the application of a pressing force and heat treatment. As shown in Fig. 2, the region occupied by voids 13 are reduced dramatically. Moreover, the fibers of polyethylene 12 are formed in a mesh-like structure extending horizontally and at random in the thickness direction with the ceramic particles 11 absorbed in the meshes formed of polyethylene fibers. Then, a metallic paste is prepared by the use of nickel powder as a metallic constituent, a binder formed of ethylcellulose, acrylic resin and butyral resin, benzylbutylphtalate as a plasticizer, and an aliphatic compound or an aromatic compound as a solvent, and the metallic paste is printed on the ceramic sheet 10b, where the porosity has been reduced already, in a desired pattern and then dried, thereby realizing a conductive layer of 2 to 2.5 μm thick.

Next, dummy layers are prepared by stacking one over another a plurality of ceramic sheets 10b, without conductive layer 2 and then a required number of a ceramic sheet 10b with a conductive layer 2 formed thereon are superimposed on the dummy layers, and further dummy layers are stacked thereon, thereby realizing a provisional laminate. At this time, it does not

matter whether the ceramic sheet 10a having the porosity not reduced or having the porosity reduced as the dummy sheets. However, when the conductive layer 2 is stacked to 100 layers or more or when the porosity of a ceramic sheet is less than 20 %, the use of the dummy sheet 10a having the porosity not reduced is preferable, because the unbalance of the pressing force caused by a difference in level existing between the region with the conductive layer 2 and the region without conductive layer 2 is relieved. With the present exemplary embodiment, the ceramic sheets 10b of 150 layer with the conductive layers 2 formed thereon are used and the dummy sheets 10a without porosity reduction are used.

After a pressing force is applied to the entire provisional laminate, the laminate is cut into a desired shape, degreased and then fired. The degreasing process is preferably to be performed by the steps of eliminating the plasticizer from the laminate while the temperature of the laminate being elevated and then eliminating the binder with the temperature of the laminate further elevated, because a new chemical compound is created from the plasticizer and binder upon quick heating the laminate at a dash in order to eliminate the plasticizer and binder at the same time, and is left in the laminate even after the degreasing process. During the step of firing, said chemical compound is burned and splashed out of the laminate and cause structural defects in the laminate such as delamination and the like, resulting in the increased number of short-circuiting failures. Furthermore, it is required to prevent the nickel contained in the conductive layer 2 from being excessively oxidized, when the steps of degreasing and firing are performed in succession.

After the step of firing, the ceramic dielectric layer 1 composing barium titanate as a major component and the conductive layer 2 composing nickel as a major component are combined together by sintering. Thereafter, the external electrodes 3 composing of copper and the like are formed, on both end surfaces of the sintered product where the conductive layer 2 exposed at the end surfaces by baking or plating process, thereby obtaining a multilayer ceramic capacitor as shown in Fig. 7.

Table 1

Porosity	Prior Art 1(50%)	40%	30%	20%	10%
Number of Short-circuiting	83/1000	2/1000	1/1000	0/1000	0/1000

Fraction defectives of multilayer ceramic capacitors, each having 150 effective layers (the number of ceramic dielectric layers 1 sandwiched between two conductive layers 2), are shown in Table 1 by comparing the multilayer ceramic capacitors prepared according to the present exemplary embodiment of

The prior art capacitor 1 is prepared according to the conventional method applied the conductive layer 2 on a ceramic sheet 10a with no reduction in porosity. As shown in Table 1, the fraction defectives due to short-circuiting are greatly reduced with the capacitors in the present exemplary embodiment when compared with the capacitor 1 according to the prior art. As the result of analyzing the cross-section of a multilayer ceramic capacitor with short circuiting failure, it is observed that the metallic path goes through the dielectric layers 1. Therefore, the present exemplary embodiment allows the metallic constituent in the conductive layer 2 to be prevented from penetrating into the ceramic sheet 10b, thereby greatly reducing the number of short circuits failures and enabling the remarkable improvement in manufacturing yields of multilayer ceramic capacitors.

Particularly, as in the present exemplary embodiment, the high polymer polyethylene used as the binder is bulky when compared with polyvinyl butyral resin and acrylic resin, making the ceramic sheet 10a rather high in porosity. Therefore, particularly in this case, reduction process of the porosity before forming the conductive layers 2 as proposed in the present invention are extremely effective in reducing failures due to short-circuiting.

(Second Exemplary Embodiment)

In the first exemplary embodiment, a conductive layer 2 is formed by printing directly a metallic paste on the ceramic sheet 10a that has already been reduced in porosity, but in the present exemplary embodiment a conductive layer 2 formed in advance on a base film 15 made of polyethyleneterephthalate and the like is transferred onto the ceramic sheet 10a.

First, the porosity of the ceramic sheet 10a is reduced in the same way as in the first exemplary embodiment, and four different ceramic sheets 10a having 40%, 30%, 20% and 10%, in porosity are prepared.

Further, as shown in Fig. 4, a conductive layer 2 is formed on the base film 15 by printing a metallic paste thereon in a desired pattern and then drying the paste. Thereafter, almost all the solvent contained in said layer 2 is released, thereby allowing the surface of the conductive layer 2 to be hardened with almost only the metallic constituent, plasticizer and binder remained in said layer 2. More specifically, said layer 2 is prepared so that the organic constituent contained may account for 5 to 15 wt%, more preferably 8 to 12 wt%, against 100wt% of the metallic constituent because, when the organic constituent is less than 5 wt %, the adhesion of said layer 2 to the ceramic sheet 10b is likely to be weak and, when the organic constituent exceeds 15 wt %, the adhesion of said layer 2 tends to become excessive, thereby making it difficult for said layer 2 to be made with a desired pattern.

At this time, in order to release the base film 15 from layer 2 easily at a later step, it is desirable for said layer 2 to be printed a release agent layer (not shown in the drawing) formed of at least one selected from a group of acrylic resin, melamine resin, epoxy resin and silicon resin beforehand on the base film 15. Particularly, the mixture of acrylic resin and melamine resin provides an excellent releasing ability. Silicon resin has both an excellent releasing ability and an excellent resistance to solvents and humidity.

Next, after having a dummy layer formed by stacking one over another a plurality of ceramic sheets 10a without conductive layers on a supporter, a conductive layer together with a base film 15 is superimposed on a ceramic sheet 10b so as to have the conductive layer 2 brought into direct contact with the ceramic sheet 10b and then a pressing force is applied from both sides thereof vertically, thereby allowing said layer 2 and ceramic sheet 10b to be adhered with each other. This process of a pressing force application is preferably performed at the temperatures ranging from room temperature to a temperature, at which the plasticizer in said layer 2 is starting to be released outside. (The temperature is 150 °C in the present exemplary embodiment.) The reason is that, when an

excessive amount of the plasticizer is released, said layer 2 tends to become hard and brittle, resulting in a reduction in the adhesion between the ceramic sheet 10b and said layer 2 and creation of structural defects at the process of stacking and firing. Accordingly, at the foregoing temperatures treatment, the binder and plasticizer contained in said layer 2 are made softer, thereby allowing the adhesion between said layer 2 and the ceramic sheet 10b to be enhanced.

When the content level of the plasticizer and that of the binder are low in said layer 2, the temperatures applied during the application of a pressing force are increased, thereby making the plasticizer and binder chemically active with resulting enhancement of the adhesion between said layer 2 and the ceramic sheet 10b. On the other hand, when the content level of the plasticizer and that of the binder are high, there is no need of additional heating since the adhesion between said layer 2 and the ceramic sheet 10b is strong enough even at room temperature. In other words, it is desirable to adjust the heating temperatures according to the kinds of organic constituents and the content levels thereof.

Then, the base film 15 is peeled off said layer 2 and the ceramic sheet 10b of the next layer is superimposed thereon. Thereafter, another said layer 2 with a base film 15 attached thereon is superimposed on the ceramic sheet 10b and a pressing force is applied thereto under the same condition as above. This stacking process of the ceramic sheet 10b and said layer 2 is repeated a desired number of times, and finally a ceramic sheet 10a is superimposed thereon to form an dummy layer.

Following the completion of the above steps, a multilayer ceramic capacitor with 150 effective layers is prepared according to the same steps as described in the first exemplary embodiment.

Table 2

Porosity	Prior Art 2(50%)	40%	30%	20%	10%
Number of Short-circuiting	21/1000	0/1000	0/1000	0/1000	0/1000

Fraction defectives of multilayer ceramic capacitors due to short-circuiting are shown in Table 2 by comparing the multilayer ceramic capacitors prepared according to the present exemplary embodiment of the invention with the capacitor by a prior art.

The multilayer ceramic capacitor by the prior art is prepared according to the foregoing method by the use of a ceramic sheet 10a without reduction process in porosity. As shown in Table 2, the short-circuiting failure are greatly reduced with the capacitors in the present exemplary embodiment when compared with the capacitor by the prior art. In addition, it is also observed that the short-circuiting failure are reduced with the multilayer ceramic capacitors in the present exemplary embodiment when compared with the capacitors in the first exemplary embodiment. This is because, in addition to the reduction in porosity, the metallic constituent is further prevented from penetrating into the ceramic sheet 10b, since the conductive layer 2 is transferred onto the ceramic sheet 10b after being dried.

As a result, according to the present exemplary embodiment, the penetration of the metallic constituent into the ceramic sheet 10b is further prevented in comparison with the first exemplary embodiment, thereby allowing the failures due to short-circuiting between the conductive layers 2 to be reduced remarkably, leading to a great improvement in yields.

When the ceramic sheet 10b and conductive layer 2 are stacked one over another alternately according to the present exemplary embodiment, a pressing force is applied to the ceramic sheet 10b every time one layer thereof is stacked. Alternatively, it is acceptable to simply stack the ceramic sheet 10b without applying any pressing force thereto and to apply a pressing force only after said layer 2 formed on a base film 15 is stacked, thereby establishing the adhesion between the ceramic sheets 10b and the adhesion between the ceramic sheet 10b and said layer 2 at the same time. In the latter process, the number of times of the pressing force application process are reduced in half compared with the former process. This pressing force application process takes about 1 to 30 seconds for one time of operation. The longer time is required in the pressing process with increasing stacked layer, so that the process cost much. Particularly,

when said layer 2 is formed of a base metal, the cost involved with stacking process accounts for rather a large portion of the cost of the multilayer ceramic capacitors. The latter process for production of provisional laminates is preferred from a point of process cost.

5 With the present exemplary embodiment also, a ceramic sheet 10a without reduction in porosity is used as the dummy layer since there are provided more than 100 layers of the conductive layer 2.

(Third Exemplary Embodiment)

10 In the present exemplary embodiment, as shown in Fig. 4, a conductive layer 2 formed on a base film 15 is superimposed on a ceramic sheet 10b together with the base film 15 so as to bring said layer 2 into contact with the ceramic sheet 10b, thus preparing the ceramic sheet 10b with said layer 2 attached thereto and producing a laminate by stacking said ceramic sheets 10b.

15 More specifically, a ceramic sheet 10b with a reduction in porosity, prepared in the same manner as in the first and second exemplary embodiments, and said layer 2 formed on a base film 15 together with the base film are sandwiched between rollers to apply a pressing force thereto, thereby bringing the ceramic sheet 10b and said layer 2 close contact with each other. The conductive layer 2 during the time of the pressing force application is held at room temperature to the temperature, at which the plasticizer contained in said layer 2 is not released excessively, in the same way as in the second exemplary embodiment.
20

Thus, said layer 2 and ceramic sheet 10b are formed on the base film 15 and, after cutting the foregoing to a desired shape, a desired number of the shapes are stacked one over another in succession and then a provisional laminate can be obtained by applying a pressing force thereto.
25

When the provisional laminate is produced, it does not matter whether repeating a process of stacking the foregoing on an dummy layer as in the first and second exemplary embodiments so as to have the base film 15 situated on the top and peeling off the base film 15 after applying a pressing force thereto or repeating a process of first eliminating the base film 15 and then stacking a ceramic sheet 10b together with said layer 2, immediately followed with an appli-
30

cation of a pressing force thereto. However, when the process of stacking is performed so as to have said layer 2 located upward after the elimination of the base film 15, it is important to take into consideration that said layer 2 does not stick to the press plate. For example, it is favorable to put some film between the conductive layer 2 and the aforementioned press plate and the like. Therefore, it is preferred that the ceramic sheet 16 is arranged to be located upward when the base film 15 is peeled off and then the process of stacking is performed.

Thereafter, a multilayer ceramic capacitor can be obtained in the same way as in the first exemplary embodiment. When a ceramic sheet 10b with said layer 2 attached thereto is prepared by the use of rollers as in above, the process can be performed at a relatively high speed. More specifically, in the third exemplary embodiment, when compared with the second exemplary embodiment, in which every time a ceramic sheet 10b and said layer 2 are stacked one over another a pressing force is applied thereto, the number of pressing force application times is allowed to be reduced in half, leading to a reduction in cost of manufacture.

Table 3

Porosity	Prior Art 2(50%)	40%	30%	20%	10%
Number of Short-circuiting	31/1000	1/1000	0/1000	0/1000	0/1000

Fraction defectives of multilayer ceramic capacitors with 150 effective Layers (the number of layers of the ceramic dielectric layer 1 sandwiched between the conductive layers 2) due to short-circuiting are shown in Table 3 by comparing the multilayer ceramic capacitors prepared according to the present exemplary embodiment of the invention with the ceramic capacitor 3 by the prior art. As clearly shown in Table 3, the fraction defectives due to short-circuiting are greatly reduced with the capacitors in the present exemplary embodiment when compared with the capacitor 3 the prior art. As the result of analyzing the cross-section of a multilayer ceramic capacitor, it is observed that the short-circuiting failure occurred between the conductive layers 2.

Therefore, the present exemplary embodiment allows the number of short circuits between conductive layers 2 to drop off sharply, thereby enabling

the remarkable improvement in manufacturing yield and the reduction in cost of the capacitors.

(Fourth Exemplary Embodiment)

In the present exemplary embodiment also, a conductive layer 2 is formed on a base film 15 made of a polyethyleneterephthalate film and the like instead of forming the conductive layer 2 directly on a ceramic sheet 10b.

First, in the same way as in the previous exemplary embodiments, four kinds of ceramic sheet with a porosity of 40 %, 30 %, 20 % and 10 % are prepared .

Further, in the same way as in the first exemplary embodiment, a metallic paste is applied onto the base film 15 by printing in a desired pattern and then dried to form the conductive layer 2. Thereafter, by having almost all the solvent contained in said layer 2 released to harden the surface thereof, thereby said layer 2 are formed of a metallic constituent, plasticizer and binder. More specifically, the organic constituents contained in said layer 2 are made to account for 5 to 15 wt% or more preferably 8 to 12 wt% against 100 wt% of the metallic constituent. The reason for above is that, when the organic constituents are less than 5 wt%, the adhesion with the ceramic sheet 10b is impaired and, on the other hand, when the organic constituents exceed 15 wt%, the adhesion of the conductive layer 2 becomes too much to make it impossible for said layer 2 to be shaped to the desired pattern.

At this time, in order to release said layer 2 and base film 15 readily from each other at a later step, it is desirable for said layer 2 to be printed after a release agent layer (not shown in the drawing) containing of at least one selected from a group of acrylic resin, melamine resin, epoxy resin and silicon resin beforehand on the base film 15. Particularly, a release agent containing of the mixture of acrylic resin and melamine resin provides a desirable releasing ability. In addition, when a silicon resin is used as the release agent, not only the desirable releasing ability is realized but also excellent resistance to solvent and humidity is effectively realized. In this case, the surface of said layer 2 have microscopic asperities as shown in Fig. 4.

Then, the conductive layer 2 together with the base film 15 after drying

is passed between rollers 16a and 16b, each having a smooth surface, as shown in Fig. 6, thereby reducing the asperities on the surface of said layer 2 with a pressing force applied in the thickness direction thereof. Fig 5 is a cross-sectional view of said layer 2 after the surface thereof is made smooth.

5 Next, after dummy layers are formed on a supporting member by stacking a plurality of ceramic sheets 10a, without conductive layer formed thereon, said layer 2 together with a base film 15 is superimposed on the dummy layers so as to bring said layer 2 into direct contact with the ceramic sheet 10b and then a pressing force is applied in the thickness direction thereof, thereby
10 allowing said layer 2 and ceramic sheet 10 to be adhered with each other. The pressing force application process is preferably to be performed at temperatures ranging from room temperature to the temperature, at which the plasticizer contained in said layer 2 is not released too much. (The temperature is 150 °C with the present exemplary embodiment.) The reason is that, when too much plasticizer is released, said layer 2 becomes so hard and brittle so as to reduce the adhesion
15 between the ceramic sheet 10b and said layer 2, resulting in bringing about structural failures at the process of stacking multilayer and firing. Therefore, as far as the foregoing temperature range is abided by, the binder and plasticizer contained in said layer 2 remain soft, thereby allowing the adhesion
20 between said layer 2 and the ceramic sheet 10b to be enhanced.

When the plasticizer and binder content in said layer 2 is low, by raising the temperature, at which a pressing force is applied thereto, the plasticizer and binder are made chemically active, thereby allowing the adhesion between said layer 2 and the ceramic sheet 10b to be enhanced. On the other
25 hand, when the plasticizer and binder content is high, the adhesion between said layer 2 and the ceramic sheet 10b is enough even at room temperature and, therefore, there is no need of additional heating. In other words, it is preferred that the heating are adjusted according to the kind and content of organic constituents contained in said layer 2.

30 Next, the base film 15 is peeled off and the ceramic sheet 10b is superimposed on said layer 2. Then, said layer 2 together with the base film 15 is again superimposed on the ceramic sheet 10b and a pressing force is applied

thereto under the foregoing condition. This stacking process of the ceramic sheet 10b and said layer 2 is performed repeatedly the required number of times to obtain a provisional laminate. Thereafter, in the same way as in the first exemplary embodiment, a multilayer ceramic capacitor with 150 effective layers is produced.

Table 4

Porosity	Prior Art 4 (50%)	40%	30%	20%	10%
Number of Short-Circuiting	9/1000	1/1000	0/1000	0/1000	0/1000

In Table 4, fraction defectives of multilayer ceramic capacitors are shown by making a comparison between the multilayer ceramic capacitors prepared according to the present exemplary embodiment and the capacitor 4 by the prior art.

The capacitor 4 by the prior art is prepared according to the method as described in the second exemplary embodiment by the use of a ceramic sheet 10a without reduction in porosity. As clearly seen in Table 4, the fraction defectives due to short-circuiting are greatly reduced with the multilayer ceramic capacitors in the present exemplary embodiment when compared with the prior art multilayer ceramic capacitor 4. In addition, it is also observed that the fraction defectives due to short-circuiting are reduced in the present exemplary embodiment even when compared with the multilayer ceramic capacitors in the second exemplary embodiment. This is because, in addition to the reduction in porosity, the metallic constituent is allowed to be further prevented from penetrating into the ceramic sheet 10b, the penetrating via the asperities of said layer 2 sticking in the ceramic sheet 10b.

As a result, according to the present exemplary embodiment, the penetration of the metallic constituent into the ceramic sheet 10b is further prevented in comparison with the second exemplary embodiment, thereby allowing the failures due to short-circuiting between said layers 2 to be reduced remarkably, leading to a great improvement in yields.

Furthermore, it does not matter whether a laminate is produced by having the ceramic sheets 10b and said layers 2 stacked one over another alternately as described in the second exemplary embodiment or by preparing first a ceramic sheet 10b with said layer 2 attached thereto as in the third exemplary embodiment and then performing the stacking thereof. Whichever method may be employed, the resulting effect is the same as in the second and third exemplary embodiments and the fraction defectives due to short-circuiting are further reduced.

Next, a description is given to key points of the present invention.

(1) The present invention is effectively applicable in particular to a ceramic sheet 10a with the porosity of exceeding 50 % before a reduction in porosity.

(2) When polyethylene is contained in ceramic sheets 10a and 10b as in each respective exemplary embodiment of above, upon having the ceramic sheets 10a and 10b left standing for a long period after a reduction is made in porosity with no heat applied thereto, air enters into the ceramic sheet 10b with a resulting increase in porosity. Therefore, when a reduction is made in porosity without heat treatment to the ceramic sheet 10b, it is preferred that a laminated process is carried promptly. When heat is applied to the ceramic sheet 10b in the process of a porosity reduction, since polyethylene undergoes a plastic deformation, the porosity of the ceramic sheet 10b does not change much even if the ceramic sheet 10b is left standing for a long period.

(3) A reduction in porosity of a ceramic sheet 10a causes the coefficient of elasticity thereof to increase, thereby allowing a ceramic sheet 10b to be prevented from being stretched in the manufacturing process.

(4) By a pressing force applied to a ceramic sheet 10a across the thickness thereof in order to make a reduction in porosity thereof, microscopic asperities on the surface of a ceramic sheet 10b, on which a conductive layer 2 is formed, are allowed to be reduced. As a result, said layer 2 can be formed on the ceramic sheet 10b easily.

(5) When a pressing force is applied at temperatures between the glass-transition temperature of an organic material in a ceramic sheet 10a and the

melting point thereof in order to make a reduction in porosity of the ceramic sheet 10a, it is preferred that the pressing force application and the heating are performed at the same time or the pressing force application precedes the heating. Particularly, when a reduction is made in porosity of a ceramic sheet 10a prepared by the use of polyethylene, it is needed that the pressing force application and the heating are performed at the same time or the pressing force application precedes the heating. The reason is that, when heating takes place to raise the temperatures higher than the glass-transition temperature of polyethylene and to keep the temperatures lower than the melting point thereof while no pressing force being applied to the ceramic sheet 10a, the ceramic sheets 10a and 10b are made to shrink in the width direction thereof due to the shrinkage of polyethylene, thereby making it difficult to obtain the ceramic sheet 10b with a uniform thickness and free of wrinkles.

(6) By reducing the porosity of a ceramic sheet 10b less than 50 %, an infiltration preventive effect against a metallic constituent in said layer 2 to penetrate into a ceramic sheet 10b is remarkable. However, when a provisional laminate and a laminated block are produced, an excessively low level of porosity makes it difficult to smooth out the difference in level between an area with a conductive layer 2 and other area without conductive layer 2, resulting in bringing about structural defects. Therefore, it is desirable for the porosity to be ranging from 10 % to 40 %.

(7) The pressing force applied to a ceramic sheet 10b with a reduction in porosity during the preparation process thereof is desired to be not exceeding the pressing force applied to the provisional laminate at a later step and more preferably to be lower than the pressing force applied to the provisional laminate. The reason is that an excessively low level of porosity in the ceramic sheet 10b before producing the provisional laminate makes it difficult to smooth out the difference in level between an area with a conductive layer 2 and an other area without the conductive layer 2, resulting in bringing about structural defects.

(8) When a heat treatment is applied during the steps to obtain a provisional laminate by the use of ceramic sheets 10a and 10b containing an organic binder, the heat treatment at temperatures ranging from the glass-transition

temperature of the organic binder and the melting point thereof, enhance the fluidity of polyethylene, results in a remarkable heat treatment effect.

(9) A ceramic sheet 10a is formed of the organic material arranged horizontally in a mesh-like structure and at random in the thickness direction, having ceramic particles 11 absorbed in the meshes. As a result, the ceramic particles 11 are arranged in layers, each also appearing to be a mesh-like structure, thereby allowing the metallic constituents to be prevented from entering into the ceramic sheet 10a.

(10) Although polyethylene of over 400,000 in average molecular weight is used as the binder in the foregoing exemplary embodiments, it does not matter whether other polyolefin arranged horizontally in a mesh-like structure and at random in the thickness direction is used in place of polyethylene.

(11) Although a description has been given to multilayer ceramic capacitors in the foregoing exemplary embodiments, the same effects can be obtained with such ceramic electronic components as a multilayer varistor, multilayer inductor, multilayer ceramic substrate, ceramic filter and the like that are produced by stacking ceramic sheets and conductive layers one over another to form a laminate.

20 Industrial Applicability

According to the present invention as described in above, by having a reduction made beforehand in porosity of a ceramic sheet, the metallic constituent in a conductive layer penetrating into the ceramic sheet is allowed to be limited to the minimum, and further by having a conductive layer formed so as to make it difficult for the metallic constituent to penetrate into the ceramic sheet, the short-circuiting failures of ceramic electronic components are allowed to be reduced to an extreme, thereby enabling the improvement in production yields. Particularly, the present invention brings about the greatest effect for the improvement of production yields in multilayer ceramic chip capacitors which require the use of thin ceramic sheets and a great extent of lamination.

CLAIMS

1. A method of manufacturing ceramic electronic components comprising:
a first step of making a reduction of porosity in a ceramic sheet containing
a ceramic powder and an organic material by applying a pressing force
thereto;
a second step of forming a conductive layer on said sheet by using a metallic
paste;
a third step of producing a laminate by stacking a plurality of said sheets,
each having said conductive layer formed thereon, in such a way as having
said conductive layers located opposite to each other and sandwiching said
conductive layer between two adjoining ceramic sheets; and
a fourth step of firing said laminate.
2. The method of manufacturing ceramic electronic components according to
Claim 1, wherein a ceramic sheet presents 50 % or more in porosity before
said first step.
3. The method of manufacturing ceramic electronic components according to
Claim 1, wherein said ceramic sheet contains at least a ceramic constituent
and polyethylene in said first step.
4. The method of manufacturing ceramic electronic components according to
Claim 1, wherein said ceramic sheet presents less than 50 % in porosity after
said first step.
5. The method of manufacturing ceramic electronic components according to
Claim 1, wherein said pressing force applied in said first step is smaller than
a pressing force applied in producing said laminate in said third step.
6. The method of manufacturing ceramic electronic components according to
Claim 1, wherein heat is applied together with said pressing force.
7. The method of manufacturing ceramic electronic components according to
Claim 6, wherein the heat treatment is carried out at temperature ranges
from a glass-transition temperature of at least one organic material con-
tained in said ceramic sheet to a melting point thereof.
8. A method of manufacturing ceramic electronic components comprising:

a first step of making a reduction of porosity in a ceramic sheet containing a ceramic powder and an organic material by applying a pressing force thereto; a second step of forming in advance a conductive layer on a base film by using a metallic paste and superimposing said conductive layer on said ceramic sheet;

a third step of producing a laminate by stacking a plurality of said ceramic sheets, each having said conductive layer superimposed thereon, in such a way as having said conductive layers located opposite to each other and sandwiching said conductive layer between two adjoining ceramic sheets; and

a fourth step of firing said laminate.

9. The method of manufacturing ceramic electronic components according to Claim 8, wherein a ceramic sheet presents 50 % or more in porosity before said first step.

10. The method of manufacturing ceramic electronic components according to Claim 8, wherein said ceramic sheet contains at least a ceramic constituent and polyethylene in said first step.

11. The method of manufacturing ceramic electronic components according to Claim 8, wherein said ceramic sheet presents less than 50 % in porosity after said first step.

12. The method of manufacturing ceramic electronic components according to Claim 8, wherein said pressing force applied in said first step is smaller than a pressing force applied in producing said laminate in said third step.

13. The method of manufacturing ceramic electronic components according to Claim 8, wherein heat is applied together with said pressing force.

14. The method of manufacturing ceramic electronic components according to Claim 13, wherein the heat treatment is carried out at temperature ranges from a glass-transition temperature of at least one organic material contained in said ceramic sheet to a melting point thereof.

15. The method of manufacturing ceramic electronic components according to Claim 8, wherein an organic constituent contained in a conductive layer before being superimposed in said third step ranges from 5 wt % to 15 wt %

against 100 wt % of a metallic constituent.

16. The method of manufacturing ceramic electronic components according to Claim 8, wherein a step of applying a pressing force to said conductive layer in a thickness direction thereof is provided between said second step and said third step.

17. A method of manufacturing ceramic electronic components comprising:

a first step of producing a laminate by stacking ceramic sheets, each containing an organic material and a ceramic powder, and conductive layers one over another alternately; and a second step of firing said laminate, wherein said ceramic sheet has said organic material arranged horizontally in a mesh-like structure, and said organic material and ceramic powder arranged horizontally in a mesh-like structure and at random in the thickness direction.

18. The method of manufacturing ceramic electronic components according to Claim 17, wherein said ceramic sheet presents less than 50 % in porosity.

19. The method of manufacturing ceramic electronic components according to Claim 17, wherein said first step consists of a repeating process comprising the steps of superimposing a conductive layer formed on a base film on said ceramic sheet and peeling off said base film; and superimposing said another ceramic sheet on said conductive layer.

20. The method of manufacturing ceramic electronic components according to Claim 19, wherein there is provided an additional step of applying a pressing force to said conductive layer before being superimposed on said ceramic sheet.

21. The method of manufacturing ceramic electronic components according to Claim 17, wherein said conductive layer is formed by using a thin film process.

22. A ceramic electronic component produced by stacking ceramic layers formed of ceramic sheets, each having an organic material arranged horizontally in a mesh-like structure, and said organic material and ceramic powder arranged at random in the thickness direction, and conductive layers one over another alternately.

23. The ceramic electronic component according to Claim 22, wherein said organic material is polyolefin.
24. The ceramic electronic component according to Claim 23, wherein said polyolefin is polyethylene.

(57) **Abstract:** A method of manufacturing ceramic includes a first step of compressing a ceramic sheet (10a) containing ceramic powder and organic to reduce porosity, a second step of forming a conductor layer (2) of metallic paste on the ceramic sheet (10b), a third step of stacking a plurality of ceramic sheets (10b) into a laminate such that each ceramic sheet (10b) is sandwiched between conductor layers (2), and a fourth step of sintering the laminate. Since the conductor layer (2) is formed on the ceramic sheet (10b) with its porosity reduced, metallic components are hindered from passing into the ceramic sheet (10b). The conductor layer can be formed by transferring onto a ceramic sheet to suppress the diffusion of the metallic components of the conductor layer. This method reduces short circuits of ceramic devices and increases the yield rate.

Fig. 1

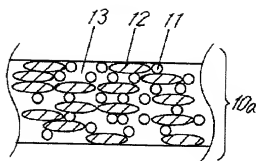


Fig. 2

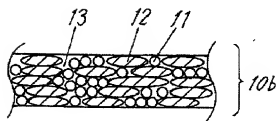


Fig. 3

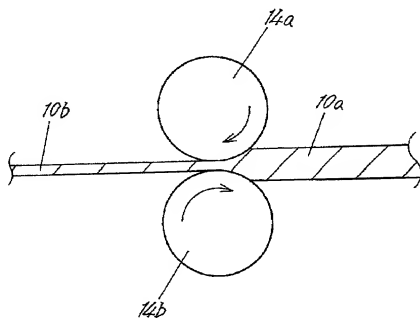


Fig. 4

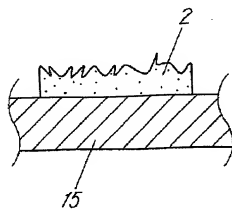


Fig. 5

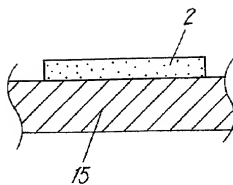


Fig. 6

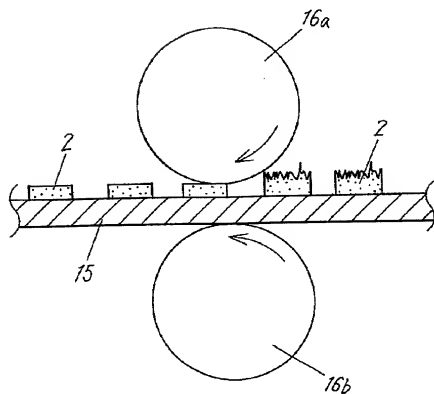
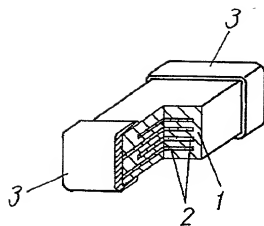


Fig. 7



Key to Reference Alphanumeric Characters

- | | |
|-----|---|
| 1 | Ceramic Dielectric Layer |
| 2 | Conductive Layer |
| 3 | External Electrode |
| 10 | Ceramic Sheet |
| 10a | Ceramic Sheet before Porosity Reduction |
| 10b | Ceramic Sheet after Porosity Reduction |
| 11 | Ceramic Particle |
| 12 | Polyethylene |
| 13 | Void |
| 14a | Roller |
| 14b | Roller |
| 15 | Base Film |
| 16a | Roller |
| 16b | Roller |

COMBINED DECLARATION FOR PATENT APPLICATION AND POWER OF ATTORNEYAttorney's Docket Number
43890-509

(Includes Reference to PCT International Application(s))

As below named inventor, I hereby declare that

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

ELECTRONIC DEVICE OF CERAMIC

the specification of which:

- ☐ is attached hereto.
- ☒ was filed as United States application Serial No. 09/830,493
- on April 27, 2001
- and was amended on _____ (if applicable).
- ☒ was filed as PCT international application Number PCT/JP00/06460
- on September 21, 2000
- and was amended under PCT Article 19 on _____ (if applicable).

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is known to me to be material to patentability in accordance with Title 37, Code of Federal Regulations, §1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, §119(a)-(d) or Section 365(b) of any foreign and/or international application(s) for patent or inventor's certificate or Section 365(a) of any PCT international application(s) designating at least one country other than the United States of America listed below and have also identified below any foreign application(s) for patent or inventor's certificate or any PCT international application(s) designating at least one country other than the United States of America filed by me on the same subject matter having a filing date before that of the application(s) of which priority is claimed

PRIOR FOREIGN/PCT APPLICATION(S) AND ANY PRIORITY CLAIMS UNDER 35 U.S.C. 119:

COUNTRY (if PCT, indicate "PCT")	APPLICATION NUMBER	DATE OF FILING (day, month, year)	PRIORITY CLAIMED UNDER 35 USC 119
JAPAN	11/268636	September 22, 1999	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No
JAPAN	11/286522	October 7, 1999	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No

I hereby claim the benefit under 35 USC §119(e) of any United States provisional application(s) listed below

PRIOR PROVISIONAL APPLICATION(S):

Application Number	Filing Date

I hereby claim the benefit under Title 35, United States Code, §120 of any United States application(s), or §365(c) of any PCT international application(s) designating the United States of America that is/are listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in that/those prior application(s) in the manner provided by the first paragraph of Title 35, United States Code, §112, I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, §1.56 which occurred between the filing date of the prior application(s) and the national or PCT international filing date of this application.

PRIOR U.S. APPLICATIONS OR PCT INTERNATIONAL APPLICATIONS DESIGNATING THE U.S. FOR BENEFIT UNDER 35 U.S.C. 120:

U S APPLICATIONS			STATUS (Check One)		
U.S. Application Number	U.S. Filing Date		Patented	Pending	Abandoned
PCT APPLICATIONS DESIGNATING THE U S					
PCT Application No.	PCT Filing Date	U.S. Serial Numbers Assigned (if any)			
PCT/JP00/06460	September 21, 2000				

POWER OF ATTORNEY: As named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith: Stephen A. Becker, Reg. No. 26,527; John G. Bisbikis, Reg. No. 37,085; Christopher D. Bright, Reg. No. 46,578; Daniel Bucca, Reg. No. 42,368; Kenneth L. Cage, Reg. No. 26,151; Jennifer Chen, Reg. No. 42,404; Bernard P. Codd, Reg. No. 46,429; Lawrence T. Cullen, Reg. No. 44,489; Paul Devinsky, Reg. No. 28,553; Margaret M. Duncan, Reg. No. 30,879; Shamita De. Etienne-Cummings, Reg. No. 46,072; Ramyar M. Farid, Reg. No. 46,692; Brian E. Ferguson, Reg. No. 36,801; Michael E. Fogarty, Reg. No. 36,139; John R. Fuisz, Reg. No. 37,327; Willem F. Gadiano, Reg. No. 37,136; Keith E. George, Reg. No. 34,111; Matthew V. Grumbling, Reg. No. 44,427; John A. Hankins, Reg. No. 32,029; Joseph Hyosuk Kim, Reg. No. 41,425; Eric J. Kraus, Reg. No. 36,190; Catherine Krupka, Reg. No. 46,227; Jack Q. Lever, Reg. No. 28,149; Raphael V. Lupo, Reg. No. 28,363; Michael A. Messina, Reg. No. 33,424; Dawn L. Palmer, Reg. No. 41,238; Joseph H. Paquin, Jr., Reg. No. 31,647; Scott D. Paul, Reg. No. 42,984; William D. Pegg, Reg. No. 42,988; Robert L. Price, Reg. No. 22,685; Gene Z. Rubinson, Reg. No. 33,351; Joy Ann G. Serauskas, Reg. No. 27,952; Daniel H. Sherr, Reg. No. 46,425; David A. Spenard, Reg. No. 37,449; Arthur J. Steiner, Reg. No. 26,106; David L. Stewart, Reg. No. 37,578; Wesley Strickland, Reg. No. 44,363; Michael D. Switzer, Reg. No. 39,552; Daniel S. Trainor, Reg. No. 43,959; Cameron K. Weiffenbach, Reg. No. 44,488; Aaron Weissstuch, Reg. No. 41,557; Edward J. Wise, Reg. No. 34,523; Jeffrey A. Woller, Reg. No. 48,041; Alexander V. Yampolsky, Reg. No. 36,324; and Robert W. Zelnick, Reg. No. 36,976, all of McDermott, Will & Emery.

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I hereby declare that all statement made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Signature of Inventor 201: Hideki Kuramitsu	Signature of Inventor 202: Atsuo Nagai	Signature of Inventor 203: Yoshiya Sakaguchi
Date July 16, 2001	Date July 16, 2001	Date July 16, 2001